PTO/SB/08A (08-03) Approved for use through 10/31/2002. OMB 0651-0031 U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Complete if Known

09/927129

8/10/2001

2662

Stephen Melvin

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Application Number

First Named Inventor

Filing Date

**Group Art Unit** 

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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**Examiner Name** Hissou Kissan MIPS.0177-00-US **Attorney Docket Number** Sheet

			U.S. PATENT	DOCUMENTS	
Examiner	Cite	Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where
Initials*	No.1	Number-Kind Code <sup>2(if known)</sup>	MM-DD-YYYY	Applicant of Cited Document	Relevant Passages or Relevant Figures Appear
MB		20040148382	07-29-2004	Narad et al.	
MB		20050061401	03-24-2005	Tokoro et al.	
MB		20040172471	09-02-2004	Kelvin R. Porter	
MB		20020124262	09-05-2002	Basso et al.	
MB		6249801	06-19-2001	Zisapel et al.	
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MD		6047122	04-04-2000	Cynthia J. Spiller	
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Examiner	Cite	Foreign Patent Document	Publication Date	Name of Patentee or	Pages, Columns, Lines,	<b>⊤</b> 6
Initials*	No. <sup>1</sup>	Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>6</sup> (d known)	MM-DD-YYYY	Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	
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Examiner Signature	Marid Bonambial	Date Considered	1/16/05

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Sheet 1 of

Cor	nplete if Known	
Application Number	09/927129	
Filing Date	8/10/2001	
First Named Inventor	STEPHEN MELVIN	
Group Art Unit	2662	
Examiner Name	HISSOU KISSAN	
Attorney Docket Number	MIPS.0177-00-US	

			U.S. PATENT	DOCUMENTS	
Examiner Cite Do		Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures
Initials*	No.1	Number-Kind Code <sup>2(II known)</sup>	MM-DD-YYYY	Applicant of Cited Document	Appear
MB		6131163	10-10-2000	Scott L. Wiegel	
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Examiner	Cite	Foreign Patent Document	Publication Date	Name of Patentee or	Pages, Columns, Lines,	T <sup>6</sup>				
	No.1	Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (* known)	MM-DD-YYYY	Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear					
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STATEMENT BY APPLICANT

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Application Number	NA					
Filing Date	08/10/2001	•				
First Named Inventor	Enrique Musoll et al.					
Group Art Unit	NA	i				
Examiner Name	NA	-5				
Attorney Docket Number	P3878	щ				

				U.S. PATENT DOC	JMENTS	. ල
Examiner Initials	Cite No.1	U.S. Patent Document  Kind Code <sup>2</sup> (if known)		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
MB		09/737,37		Mario Nemirovsky et al.	NA	Priority Claim
MA		60/181,36	4	Mario Nemirovsky et al.	NA	Priority Claim
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Examiner Initials	Cite No.1	Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>s</sup> (if known)	Applicant of Cited Document	Cited Document MM-DD-YYYY	Passages or Relevant Figures Appear	T <sup>6</sup>			
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		OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS	 Ē
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		ENRIQUE MUSOLL ET AL.  Mechanism to Activate a Context When No Stream is Running in a Multi-Streaming Processing Core April 16, 2001, Disclosure Document #492431, USPTO	
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Application Number 09/927,129 Filing Date 08/10/2001 First Named Inventor Enrique Musoll Art Unit 2662 **Examiner Name** Hassan Kizou P3828 Attorney Docket Number

Examinar Intilater	Citie No.1	Document Number  Number-Kind Code <sup>2 gracem</sup>	Publication Date  MM-DD-YYYY	Name of Palentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Rolevant Figures Appear
MY		US- 6088745 A *	07-11-2000	Bertagna et al.	
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NB		<sup>US-</sup> 5812810 A	09-22-1998	Sager, David	
UB		US-5987578 A *	11-16-1999	Butcher, Lawrence	1

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<sup>\*</sup> References not provided under §1.98

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STA	TEMENT	BY A	PPLICANT	First Named Inventor	Enrique Musoll	
	(Uso as many si			Art Unit	2662	
	(USO 45 Many SI	100LD 00 /A	Ceasery)	Examiner Name	Hassan Kizou	
Sheet	3	of	3	Attorney Docket Number	P3828	

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
MB		* KNUTH, DONALD E., "The Art of Computer Programming, Third Edition, Volume 1, Fundamental Algorithms", "Sec. 2.5 Dynamic Storage Allocation", 1997, pp 435-456, Addison-Wesley, US	
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Substitute for form 1449A	PIO			Application Number	09/927129	
THADE YNFORMATION DISCLOSURE STATEMENT BY APPLICANT				Filing Date	8/10/2001	
				First Named Inventor	STEPHEN MELVIN	
				Group Art Unit	2662	
(use as many sheets as necessary				Examiner Name	HISSOU KISSAN	
Sheet	1	of	4	Attorney Docket Number	MIPS.0177-00-US	

			U.S. PATEN	T DOCUMENTS	
Examiner Initials*	Cite No.1	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figure
. 73	No.	Number-Kind Code <sup>2(if known)</sup>		· · · · · · · · · · · · · · · · · · ·	Appear
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STATEMENT BY APPLICANT
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Sheet 2 of

Cor	nplete if Known	
Application Number	09/927129	_
Filing Date	8/10/2001	_
First Named Inventor	STEPHEN MELVIN	
Group Art Unit	2662	
Examiner Name	HISSOU KISSAN	
Attorney Docket Number	MIPS.0177-00-US	

	U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite	Cite Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where		
	No. <sup>1</sup>	Number-Kind Code <sup>2(if known)</sup>	MM-DD-YYYY	Applicant of Cited Document	Relevant Passages or Relevant Figures Appear		
MB		20040172504	09-02-2004	Balazich et al.			
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NB		09/608,750	NA	Nemirovsky et al.	•		
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Initials*	No.1	Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5 (il known)</sup>	MM-DD-YYYY	Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	
MB	AA	WO 03/005645A2	06-25-2002	Sampath et al.	,	

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MB	ВА	MELVIN ET AL., "Extended Instruction Set for a Packet Processing Applications," July 5, 2001, Disclosure Document #496559, USPTO	
MB	ВВ	MUSOLL ET AL., "Hardware Algorithm for Allocating and De-Allocating Consecutive Blocks of Memory," April 3, 2001, Disclosure Document #491557, USPTO	
MB	вс	MUSOLL ET AL., "Mechanism to Overflow Packets to a Software Controlled Memory When They Do Not Fit into a Hardware Controlled Memeory,", July 3, 2001, Disclosure Document #496391, USPTO	
NB	BD	MUSOLL ET AL., "Mechanism for Allowing a Limited Packet Head and/or Tail Growth Without Moving the Packet to a Different Memeory Location," April 16, 2001, Disclosure Document #492429, USPTO	

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<sup>&</sup>lt;sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

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Sheet 3 of

Complete if Known					
Application Number	09/927129				
Filing Date	8/10/2001				
First Named Inventor	STEPHEN MELVIN				
Group Art Unit	2662				
Examiner Name	HISSOU KISSAN				
Attorney Docket Number	MIPS.0177-00-US				

		NON PATENT LITERATURE DOCUMENTS	
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Mp	BE	MUSOLL, ENRIC, "Functional Validation of a Packet Management Unit," May 18, 2001, Disclosure Document #429011, USPTO	
MB	BF	SAMPATH ET AL., "Mechanism to Un-speculatively Pre-fetch Instructions from the Thread Associated to a Packet," April 2, 2001, Disclosure Document #491527, USPTO	
MB	BG	YAMAMOTO, WAYNE. An Analysis of Multistreamed, Superscalar Processor Architectures. University of California Santa Barbara Dissertation. December 1995. Santa Barbara, US.	
Mp	вн	YAMAMOTO ET AL. "Increasing Superscalar Performance Through Multistreaming." Parallel Architectures and Compilation Techniques (PACT '95). 1995	
MP	Bi	The PowerPC Architecture: A Specification for a New Family of RISC Processors. 2 <sup>nd</sup> Ed. May 1994. pp. 70-72. Morgan Kaufmann. San Francisco, US.	
MB	BJ	MC68020 32-Bit Microprocessor User's Manual. 3 <sup>rd</sup> Ed 1989. pp. 3-125, 3-126, and 3-127. Prentice Hall, NJ, US.	
MB	ВК	POTEL, M. J. "Real-Time Playback in Animation Systems." Proceedings of the 4 <sup>th</sup> Annual Conference on Computer Graphics and Interactive Techniques. 1977. pp. 72-77. San Jose, CA, US.	
MB	BL	ARM Architecture Reference Manual. 1996. pp. 3-41, 3-42, 3-43, 3-67, and 3-68. Prentice Hall, NJ, US.	
MD	вм	ESA/390 Principles of Operation. IBM Online Publications Center Reference Number SA22-7201-08. Table of Contents and paras. 7.5.31 and 7.5.70. IBM Corporation. Boulder, CO, US.	
MD	BN	MC88110 Second Generation RISC Microprocessor User's Manual. 1991. pp. 10-66, 10-67, and 10-71.  Motorola, Inc.	
MB	ВО	DIEFENDORFF ET AL. "Organization of the Motorola 88110 Superscalar RISC Microprocessor." IEEE Journal of Microelectronics. April 1992. pp. 40-63. Vol. 12, No. 2. IEEE. New York, NY, US.	

Examiner Ma,	$\mathcal{U}_{-}$ .	Date	11.46/0-
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MB	BP	KANE, GERRY. PA-RISC 2.0 Architecture. 1996, pp. 7-106 and 7-107. Prentice Hall. NJ, US.	
MB	BQ	DIEFENDORFF ET AL. "AltiVec Extension to PowerPC Accelerates Media Processing." IEEE Journal of Microelectronics. Vol. 20, No. 2 (2000): pp. 85-95	
MB	BR	GRUNEWALD ET AL. "Towards Extremely Fast Context Switching in a Block Multithreaded Processor." Proceedings of EUROMICRO 22, 1996. pp. 592-599	
MB	BS	BRADFORD ET AL. "Efficient Synchronization for Multithreaded Processors." Workshop on Multithreaded Execution, Architecture, and Compilation. Jan-Feb 1998. pp. 1-4	
MB	вт	PAI ET AL. "An Evaluation of Memory Consistency Models for Shared-Memory Systems with ILP Processors." Proceedings of ASPLOS-VII, Oct. 1996: pp. 12-23, ACM, Inc.	
MB	BU	YOAZ ET AL. "Speculation Techniques for Improving Load Related Instruction Scheduling." 1999. Pages 42-53, IEEE.	
MD	в٧	KESSLER, R. E. "The Alpha 21264 Microprocessor: Out-of-Order Execution at 600 MHz." August 1998.	
МВ	BW	DONALSON ET AL. "DISC: Dynamic Instruction Stream Computer, An Evaluation of Performance." 26th Hawaii Conference on Systems Sciences. Vol. 1. 1993. Pages 448-456.	
MB	вх	NEMIROVSKY ET AL. "DISC: Dynamic Instruction Stream Computer." ACM. 1991. Pages 163-171	
MB	BY	MUSOLL ET AL. Mechanism to Prevent the Download of a Packet with Pending Writes Affecting Its Data. April 11, 2001. Disclosure Document #492430, USTPO	
MB	BZ	UNGERER ET AL. A Survey of Processors with Explicit Multithreading. ACM Computing Surveys, Vol., 35, No. 1. March 2003. Pages 29-63.	

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